

CURRICULUM VITAE



SHASHIKANT VERMA

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Contact No.

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Career Objective:

Seeking a position to utilize my skills and abilities in the Technical Education Industry that offers professional growth while being resourceful, innovative and flexible.

Educational Qualification:

Technical/Professional (B.Tec./B.E. onwards) :

Degree / Certificate	Board / University	Year of Passing	Subjects
B.E	M.A.N.I.T Bhopal	May 1999	Electronic & Communication
M.Tech(Electronic & Communication)	M.I.T.S Gwalior (M.P).	July –August 2006	with specialization in Microwave Engineering
Certificate course	WINTECH COMPUTERS DELHI	31 st July 1999	C, JAVA, ASP, HTML Oracle

Experience:

- About 3 years of teaching experience as a Lecturer in Bundelkhand University Jhansi (U.P) from 19th August 2001 to 23rd August 2004. During this period with teaching also worked as **System Analyst** in the university Computer Lab.
- Worked as a assistant Professor and Head in the Department of ECE SRITS Datia from 1st of august 2006 to 3rd of October 2007.
- Worked as a assistant Professor Department of ECE IVSIT Mathura from 4st of October 2007 to 3rd of 20th Febuary 2008.
- Worked as a assistant Professor and Head in the Department of ECE EIMT Mathura from 4st of august 2008 to 8th of March 2009.
- Worked as a assistant Professor and Head in the Department of ECE SRITS Datia from 9st of March 2009 to 9th of October 2013.

Post Held	Organization/ Institution	Pay Scale	Last Gross Drawn	Period		Total Period
				From	To	
Lecturer	Bundelkhand University Jhansi	8000- 13500	8000	19 th Aug. 2001	23 rd Sept. 2004	3 -Years
Assistant Professor and Head	SRITS Datia	8000- 13500	8500	4 th July 2006	3 rd October 2007	1-Year 3 - Months
Assistant Professor and	IVSIT Mathura	8000- 13500	29285	4 th October 2007	20 th february 2008	4 Months
Assistant Professor and Head	EIMT Mathura	8000- 13500	30,125	4 th August 2008	8 th March 2009	7 Months
Assistant Professor and Head	SRITS Datia	15600- 39100	32,600	9 th March 2009	9 th October 2013	4 Years 7 Months
Assistant Professor	Bundelkhand University Jhansi	15600- 39100	29012	10 th October 2013	Till Date	9 Yr 1 Months

Administrative Assignments:

- Internal Observer of Faizabad center for C.P.M.T Entrance Exam 2002.
- Internal Observer of Banda center for B.Ed Entrance Exam 2003.
- Internal Observer of Banda center for University Entrance Exam 2003
- Coordinator of “ Energy Park” SRITS Datia in 2006. Funded by Ministry of Conservation of Energy India.
- Formed a New “Renewable Energy Club” at SRITS Datia in 2006 . The work of this club is to aware the people live near Datia and in rural area about the Renewable resources.
- Co-Coordinator of TGEQIP-III Project at IET, Bundelkhand University Jhansi.

Software Proficiency:

- Languages : C, JAVA, Embedded C, Python
- Front End : VB 6.0
- Operating Systems : MS DOS, Windows
- Web Designing : ASP, HTML
- Simulation software : P-CAD, IE3D(Electromagnetic Simulator)

Extra Qualification :

Done a sixth month course in software from WINTIC Institute Munirica Centre New Delhi in 2000.
Done Online Internship Program on “FUNDAMENTALS OF IOT AND DATA SCIENCE” from 19th Aug to 3rd Oct 2020.

Achievements:

- Done UGC Sponsored refresher course in Management organized by Institute of management , Bundelkhand University, Jhansi

- Presented a paper on “Role of Fiber optics in Telecommunication and IT” in the fifth “conference of the International academy of Physical Science” (CONIAPS) April 07 to 09 , 2002. Hosted by Department of Mathematical Sciences and Computers applications , Bundelkhand University Jhansi.
- Participated in state level Vinu Manked(Under sixteen) and C.K Naidu(Under nineteen)cricket tournament .
- Member of organizing committee of state level Vinu Manked(Under sixteen) cricket tournament (1988-1989)

Projects:

- **M.Tech. Project:**

Title: Design of Microstrip Antenna in UHF band and enhance their Band Width.

Description: Personal Mobile communication and other miniaturized communication system need a small-sized microstrip antennas. The size of rectangular shaped microstrip antenna operating in U.H.F. band is quite large because its resonant length is inversely proportional to frequency. Compact MSA can be design with substrate having a higher dielectric constant ϵ_r . In this case the , the size of the rectangular shape MSA will be much smaller than that of the low dielectric constant substrates, at a given resonant frequency, but the band width is small. We design various compact MSA configuration that are obtained by modifying regular shapes, such as rectangular, circular by using shorting posts or by cutting slots in the metallic patch.

- **B.E Project:**

Title: Microprocessor Based switching network.

Description: In this project we used 8085 microprocessor and A/D and D/A converter.

In this data is taken from one channel and the transfer on to the different lines using switching technique.

Extra curricular Activities:

- Attend Workshop on patent Awareness(PAT-2003) held at institute of information technology Bundelkhand University Jhansi.
- Member of Organizing committee (Lightning & sounds) of Annual function “MAFFIC”-97.
- Represent the inter engineer college tournament ,Organized by Government Engineer college Jbalpur(M.P).
- Member of Electronic Engineering Association M.A.N.I.T.

Hobbies

PCB Designing ,Listening Music and Playing Cricket .

Personal Details:

Fathers name	: T.D. Verma
Date of Birth	: 1 st August 1971
Nationality	: Indian
Marital Status	: Married
Residential Address	: 94/1 “B” A , Khati Baba Isai Tola Jhansi (U.P) PIN[284003]
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E-Mail	: shashi_kant_1@yahoo.com

Place: Jhansi

Shashikant Verma